

# Interim Report Section H

## Design Submission Form for Team:

### Electronic Design Submission

Design submitted OK

Submitted by (ecsid): \_\_\_\_\_

Date of submission: \_\_\_\_\_

Design NOT submitted

We have discussed this with Iain McNally  
and have obtained his signature below:

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I McNally

### Paper Deliverables

Interim report section A  Text description of your design, no more than one side of A4.

Interim report section B  Circuit diagram showing your design before the addition of testability.

Interim report section C  Annotated circuit diagram showing exactly how your design is implemented.

Interim report section D  CGA Template for your design (filled in).

Interim report section E  Netlist for your design.

Interim report section F  Pinout for your design.

Interim report section G  Simulation results including a waveform plot.

Interim report section H  This form.

### Team Members

	Name	% Effort (sums to 100)	Description of individual contribution	Signature
1				-----
2				-----
3				-----
4				-----
5				-----
6				-----

*Please fill in all parts of this form.*

***Marks will be lost where deliverables are missing, late or not as specified.***

***In addition to the two submitted copies, it is strongly advised that each team member should have their own copy of this report for reference during the later stages of this design exercise.***