

## VLSI Design Project

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4+ lectures      Monday 11:00-12:00  
                         Thursday 15:00-16:00

10 lab sessions      Friday 14:00-17:00

# ELEC6231 VLSI Design Project

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## Course Structure

### Tools and Techniques Phase

- Learn AMS 0.35um ASIC Design Flow
- Implement Dice Chip using AMS 0.35um Flow
- Individual Exercise
- 20% of module mark

### Design and Implementation Phase

- Design and Implement Large Digital Chip
- Team Exercise
- 80% of module mark

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## Lecture Plan<sup>1</sup>

Week	Day	Time	Topic
1	Monday	11:00	<b>Synthesis and Constraints</b> <i>This lecture</i>
	Thursday	15:00	–
2	Monday	11:00	<b>Place and Route</b>
	Thursday	15:00	<b>System-on-Chip Design</b>
3	Monday	11:00	–
	Thursday	15:00	–
4	Monday	11:00	<b>Introduction to Group Exercise</b>

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<sup>1</sup>If there are any lectures after the first four weeks, they will be advertised via the course e-mail list

## AMS 0.35 $\mu$ m ASIC Design Flow

