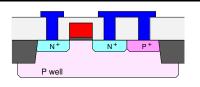
ecs.gg/iquiz

### Question 0



Q0. This is a valid cross-section of:<sup>1</sup> A. a PMOS transistor for an N-Well Process

**B.** a PMOS transistor for a Twin-Tub Process

C. a PMOS transistor for a P-Well Process

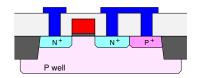
D. an NMOS transistor for an N-Well Process

E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

**G.** none of the above (it is not a valid CMOS transistor)

<sup>1</sup>select **all** options that apply



Q0. This is a valid cross-section of:<sup>1</sup> A. a PMOS transistor for an N-Well Process

B. a PMOS transistor for a Twin-Tub Process

**C.** a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

E. an NMOS transistor for a Twin-Tub Process

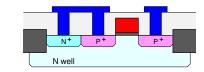
F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

<sup>1</sup>select **all** options that apply

1

### Question 1



Q1. This is a valid cross-section of:<sup>2</sup>

A. a PMOS transistor for an N-Well Process

**B.** a PMOS transistor for a Twin-Tub Process

**C.** a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

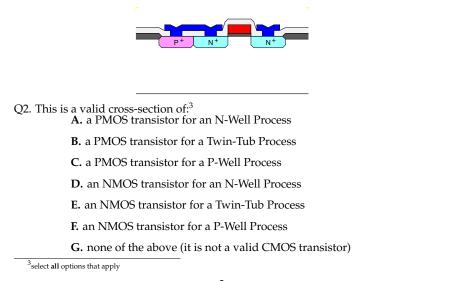
E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

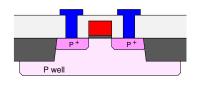
<sup>2</sup>select **all** options that apply

### Question 2



3

# Question 3



Q3. This is a valid cross-section of:<sup>4</sup> A. a PMOS transistor for an N-Well Process

**B.** a PMOS transistor for a Twin-Tub Process

C. a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

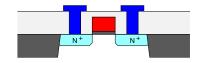
E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

**G.** none of the above (it is not a valid CMOS transistor)

<sup>4</sup>select **all** options that apply

# Question 4



- Q4. This is a valid cross-section of:<sup>5</sup> A. a PMOS transistor for an N-Well Process
  - B. a PMOS transistor for a Twin-Tub Process

**C.** a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

E. an NMOS transistor for a Twin-Tub Process

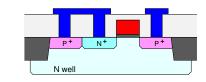
F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

<sup>5</sup>select **all** options that apply

5

# Question 5



Q5. This is a valid cross-section of:<sup>6</sup>

A. a PMOS transistor for an N-Well Process

**B.** a PMOS transistor for a Twin-Tub Process

**C.** a PMOS transistor for a P-Well Process

D. an NMOS transistor for an N-Well Process

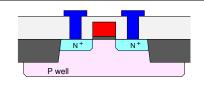
E. an NMOS transistor for a Twin-Tub Process

F. an NMOS transistor for a P-Well Process

G. none of the above (it is not a valid CMOS transistor)

<sup>6</sup>select **all** options that apply

### Question 6



Q6. This is a valid cross-section of:<sup>7</sup> A. a PMOS transistor for an N-Well Process

**B.** a PMOS transistor for a Twin-Tub Process

**C.** a PMOS transistor for a P-Well Process

**D.** an NMOS transistor for an N-Well Process

E. an NMOS transistor for a Twin-Tub Process

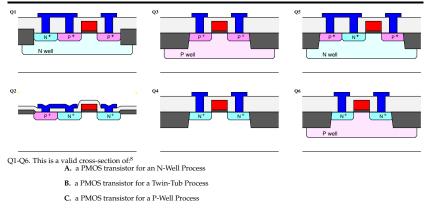
F. an NMOS transistor for a P-Well Process

**G.** none of the above (it is not a valid CMOS transistor)

<sup>7</sup>select **all** options that apply

7

### Questions 1-6



- $\mathbf{D.}\$ an NMOS transistor for an N-Well Process
- E. an NMOS transistor for a Twin-Tub Process
- F. an NMOS transistor for a P-Well Process
- G. none of the above (it is not a valid CMOS transistor)

<sup>8</sup>select **all** options that apply

8